

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
BORGATTI ET AL.)
Serial No. **10/798,401**) Examiner: **E. COLEMAN**
Confirmation No. **3793**)
Filing Date: **JANUARY 30, 2004**) Art Unit: **2183**
For: **RECONFIGURABLE SIGNAL**)
 PROCESSING IC WITH AN EMBEDDED)
 FLASH MEMORY DEVICE)

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AMENDMENT AFTER FINAL

MS AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Examiner's final Office Action of September 19, 2007, please enter the amendments and remarks set forth below. No new issues are raised by this amendment.